Digital I/O Board with Opto-Isolation for PCI
PIO-32/32L(PCI)H
Digital Input Board with Opto-Isolation for PCI
PI-64L(PCI)H
Digital Output Board with Opto-Isolation for PCI
PO-64L(PCI)H
User’s Guide
Check Your Package

Thank you for purchasing the CONTEC product.
The product consists of the items listed below.
Check, with the following list, that your package is complete. If you discover damaged or missing items, contact your retailer.

Product Configuration List
- Board (One of the following)
  [PIO-32/32L(PCI)H, PI-64L(PCI)H, or PO-64L(PCI)H] …1
- First step guide … 1
- CD-ROM *1 [API-PAC(W32)] …1

*1 The CD-ROM contains the driver software and User’s Guide (this guide)
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1. Before Using the Product

This chapter provides information you should know before using the product.

About the Board

This product is a PCI bus-compliant interface board used to provide a digital signal I/O function on a PC. This product can input and output digital signals at 12 - 24VDC.

PIO-32/32L(PCI)H features 32 opto-coupler isolated inputs and 32 opto-coupler isolated open-collector outputs. You can use 32 input signals as interrupt inputs. In addition, the digital filter function to prevent wrong recognition of input signals is provided and output transistor protection circuit (surge voltage protection and overcurrent protection).

PI-64L(PCI)H features 64 opto-coupler isolated inputs. You can use 32 input signals as interrupt inputs. In addition, the digital filter function to prevent wrong recognition of input signals is provided.

PO-64L(PCI)H features 64 opto-coupler isolated open-collector outputs. In addition, output transistor protection circuit (surge voltage protection and overcurrent protection).

Windows/Linux driver is bundled with this product.

Possible to be used as a data recording device for LabVIEW, with dedicated libraries.

Features

- Opto-coupler isolated input (compatible with current sink output) and opto-coupler isolated open-collector output (current sink type)

PIO-32/32L(PCI)H has the 32ch of opto-coupler isolated input (compatible with current sink output) and 32ch of opto-coupler isolated open-collector output (current sink type) whose response speed is 200µsec. Common terminal provided per 16channels, capable of supporting a different external power supply. Supporting driver voltages of 12 - 24 VDC for I/O.

PI-64L(PCI)H has the 64ch of opto-coupler isolated input (compatible with current sink output) whose response speed is 200µsec. Common terminal provided per 16channels, capable of supporting a different external power supply. Supporting driver voltages of 12 - 24 VDC for I/O.

PO-64L(PCI)H has the 64ch of opto-coupler isolated open-collector output (current sink type) whose response speed is 200µsec. Common terminal provided per 16channels, capable of supporting a different external power supply. Supporting driver voltages of 12 - 24 VDC for I/O.

- Opto-coupler bus isolation

As the PC is isolated from the input and output interfaces by opto-couplers, this product has excellent noise performance.

- You can use 32 input signals as interrupt request signals.

(For PIO-32/32L(PCI)H, PI-64L(PCI)H only)

You can use 32 input signals as interrupt request signals and also disable or enable the interrupt in bit units and select the edge of the input signals, at which to generate an interrupt.
1. Before Using the Product

- Windows/Linux compatible driver libraries are attached.

Using the attached driver library API-PAC(W32) makes it possible to create applications of Windows/Linux. In addition, a diagnostic program by which the operations of hardware can be checked is provided.

- This product has a digital filter to prevent wrong recognition of input signals from carrying noise or a chattering. (For PIO-32/32L(PCI)H, PI-64L(PCI)H only)

This product has a digital filter to prevent wrong recognition of input signals from carrying noise or a chattering. All input terminals can be added a digital filter, and the setting can be performed by software.

- Output circuits include zener diodes for surge voltage protection and poly-switches for overcurrent protection. (For PIO-32/32L(PCI)H, PO-64L(PCI)H only)

Zener diodes are connected to the output circuits to protect against surge voltages. Similarly, polyswitches are fitted to each group of 8 channels outputs for over-current protection. The output rating is max. 35VDC, 100mA per channel.

- LabVIEW is supported by a plug-in of dedicated library VI-DAQ.

Using the dedicated library VI-DAQ makes it possible to make a LabVIEW application.
Support Software

You should use CONTEC support software according to your purpose and development environment.

Windows version of digital I/O driver **API-DIO(WDM) / API-DIO(98/PC)**
[Stored on the bundled CD-ROM driver library API-PAC(W32)]

The API-DIO(WDM) / API-DIO(98/PC) is the Windows version driver library software that provides products in the form of Win32 API functions (DLL). Various sample programms such as Visual Basic and Visual C++, etc and diagnostic program useful for checking operation is provided.

**< Operating environment >**

You can download the updated version from the CONTEC’s Web site (http://www.contec.com/apipac/). For more details on the supported OS, applicable language and new information, please visit the CONTEC’s Web site.

Linux version of digital I/O driver **API-DIO(LNX)**
[Stored on the bundled CD-ROM driver library API-PAC(W32)]

The API-DIO(LNX) is the Linux version driver software which provides device drivers (modules) by shared library and kernel version. Various sample programs of gcc are provided.

**< Operating environment >**
- OS: RedHatLinux, TurboLinux
  (For details on supported distributions, refer to Help available after installation.)
- Adaptation language: gcc

You can download the updated version from the CONTEC’s Web site (http://www.contec.com/apipac/). For more details on the supported OS, applicable language and new information, please visit the CONTEC’s Web site.

Data acquisition VI library for LabVIEW **VI-DAQ** (Available for downloading (free of charge) from the CONTEC web site.)

This is a VI library to use in National Instruments LabVIEW.

VI-DAQ is created with a function form similar to that of LabVIEW's Data Acquisition VI, allowing you to use various devices without complicated settings.

See http://www.contec.com/vidaq/ for details and download of VI-DAQ.
1. Before Using the Product

**Cable & Connector (Option)**

96-Pin Shield Cable with a Half-Pitch Connector
- PCB96PS-0.5P (0.5m)
- PCB96PS-1.5P (1.5m)
- PCB96PS-5P (5m)

Flat Cable with 96-Pin Half-Pitch Connectors at Both Ends
- PCB96P-1.5 (1.5m)
- PCB96P-3 (3m)
- PCB96P-5 (5m)

96-Pin Shield Cable with 2Sided Half-Pitch Connector
- PCA96PS-0.5P (0.5m)
- PCA96PS-1.5P (1.5m)
- PCA96PS-3P (3m)
- PCA96PS-5P (5m)

Flat Cable with One 96-Pin Half-Pitch Connector
- PCA96P-1.5 (1.5m)
- PCA96P-3 (3m)
- PCA96P-5 (5m)

Connection Conversion Shield Cable (96P → 37P x 2)
- PCB96WS-1.5P (1.5m)
- PCB96WS-3P (3m)
- PCB96WS-5P (5m)

Half Pitch 96P Female Connector Set (5 Pieces)
- CN5-H96F

**Accessories (Option)**

Screw Terminal (M3 x 96P)
- EPD-96A *1*4

Screw Terminal (M3.5 x 96P)
- EPD-96 *1

Digital I/O 64CH Series Terminal Panel (M3 x 96P)
- DTP-64A *1

Signal Monitor for Digital I/O (64Bits)
- CM-64 L *1

Screw Terminal (M3 x 37P)
- EPD-37A *2*4

Screw Terminal (M3.5 x 37P)
- EPD-37 *2

General Purpose Terminal (M3 x 37P)
- DTP-3C *2

Screw Terminal (M2.5 x 37P)
- DTP-4C *2

Signal Monitor for Digital I/O (32Bits)
- CM-32 L *2

Connection Conversion Board (96-P → 37-P x 2)
- CCB-96 *3

*1 PCB96P or PCB96PS optional cable is required separately.
*2 PCB96WS optional cable is required separately.
*3 Option cable PCB96P or PCB96PS, and the cable for 37-pin D-SUB are required separately.
*4 “Spring-up” type terminal is used to prevent terminal screws from falling off.

* Check the CONTEC’s Web site for more information on these options.
1. Before Using the Product

Customer Support
CONTEC provides the following support services for you to use CONTEC products more efficiently and comfortably.

Web Site
Japanese  http://www.contec.co.jp/
English  http://www.contec.com/
Chinese  http://www.contec.com.cn/

Latest product information
CONTEC provides up-to-date information on products.
CONTEC also provides product manuals and various technical documents in the PDF.

Free download
You can download updated driver software and differential files as well as sample programs available in several languages.

Note! For product information
Contact your retailer if you have any technical question about a CONTEC product or need its price, delivery time, or estimate information.

Limited Three-Years Warranty
CONTEC Interface boards are warranted by CONTEC CO., LTD. to be free from defects in material and workmanship for up to three years from the date of purchase by the original purchaser.

Repair will be free of charge only when this device is returned freight prepaid with a copy of the original invoice and a Return Merchandise Authorization to the distributor or the CONTEC group office, from which it was purchased.

This warranty is not applicable for scratches or normal wear, but only for the electronic circuitry and original boards. The warranty is not applicable if the device has been tampered with or damaged through abuse, mistreatment, neglect, or unreasonable use, or if the original invoice is not included, in which case repairs will be considered beyond the warranty policy.

How to Obtain Service
For replacement or repair, return the device freight prepaid, with a copy of the original invoice. Please obtain a Return Merchandise Authorization Number (RMA) from the CONTEC group office where you purchased before returning any product.

* No product will be accepted by CONTEC group without the RMA number.

Liability
The obligation of the warrantor is solely to repair or replace the product. In no event will the warrantor be liable for any incidental or consequential damages due to such defect or consequences that arise from inexperienced usage, misuse, or malfunction of this device.
Safety Precautions
Understand the following definitions and precautions to use the product safely.

Safety Information
This document provides safety information using the following symbols to prevent accidents resulting in injury or death and the destruction of equipment and resources. Understand the meanings of these labels to operate the equipment safely.

| ⚠️ DANGER | DANGER indicates an imminently hazardous situation which, if not avoided, will result in death or serious injury. |
| ⚠️ WARNING | WARNING indicates a potentially hazardous situation which, if not avoided, could result in death or serious injury. |
| ⚠️ CAUTION | CAUTION indicates a potentially hazardous situation which, if not avoided, may result in minor or moderate injury or in property damage. |
Handling Precautions

⚠️ DANGER
Do not use the product where it is exposed to flammable or corrosive gas. Doing so may result in an explosion, fire, electric shock, or failure.

⚠️ CAUTION
- There are switches on the board that need to be set in advance. Be sure to check these before installing the board.
- Only set the switches and jumpers on the board to the specified settings. Otherwise, the board may malfunction, overheat, or cause a failure.
- Do not strike or bend the board. Doing so could damage the board. Otherwise, the board may malfunction, overheat, cause a failure or breakage.
- Do not touch the board's metal plated terminals (edge connector) with your hands. Otherwise, the board may malfunction, overheat, or cause a failure. If the terminals are touched by someone's hands, clean the terminals with industrial alcohol.
- Do not install or remove the board to or from the slot while the computer's power is turned on. Otherwise, the board may malfunction, overheat, or cause a failure. Doing so could cause trouble. Be sure that the personal computer or the I/O expansion unit power is turned off.
- Make sure that your PC or expansion unit can supply ample power to all the boards installed. Insufficiently energized boards could malfunction, overheat, or cause a failure.
- The specifications of this product are subject to change without notice for enhancement and quality improvement. Even when using the product continuously, be sure to read the manual and understand the contents.
- Do not modify the product. CONTEC will bear no responsibility for any problems, etc., resulting from modifying this product.
- Regardless of the foregoing statements, CONTEC is not liable for any damages whatsoever (including damages for loss of business profits) arising out of the use or inability to use this CONTEC product or the information contained herein.
Environment

Use this product in the following environment. If used in an unauthorized environment, the board may overheat, malfunction, or cause a failure.

Operating temperature
0 - 50°C

Humidity
10 - 90%RH (No condensation)

Corrosive gases
None

Floating dust particles
Not to be excessive

Inspection

Inspect the product periodically as follows to use it safely.

- Check that the bus connector of the board and its cable have been plugged correctly.
- Check that the board has no dust or foreign matter adhering.
- The gold-plated leads of the bus connector have no stain or corrosion.

Storage

When storing this product, keep it in its original packing form.

1. Put the board in the storage bag.
2. Wrap it in the packing material, then put it in the box.
3. Store the package at room temperature at a place free from direct sunlight, moisture, shock, vibration, magnetism, and static electricity.

Disposal

When disposing of the product, follow the disposal procedures stipulated under the relevant laws and municipal ordinances.
2. Setup

This chapter explains how to set up the board.

What is Setup?

Setup means a series of steps to take before the product can be used. Different steps are required for software and hardware. The setup procedure varies with the OS and applications used.

Using the Board under Windows

Using the Driver Library API-PAC(W32)

This section describes the setup procedure to be performed before you can start developing application programs for the board using the bundled CD-ROM “Driver Library API-PAC(W32)”. Taking the following steps sets up the software and hardware. You can use the diagnosis program later to check whether the software and hardware function normally.

- Step 1 Installing the Software
- Step 2 Setting the Hardware
- Step 3 Installing the Hardware
- Step 4 Initializing the Software
- Step 5 Checking Operations with the Diagnosis Program

If Setup fails to be performed normally, see the “Setup Troubleshooting” section at the end of this chapter.

Using the Board under Windows

Using Software Other than the Driver Library API-PAC(W32)

For setting up software other than API-PAC(W32), refer to the manual for that software. See also the following parts of this manual as required.

- This chapter Step 2 Setting the Hardware
- This chapter Step 3 Installing the Hardware
- Chapter 3 External Connection
- Chapter 6 About Hardware
Using the Board under an OS Other than Windows

For using the board under Linux, see the following parts of this manual.

- This chapter Step 2 Setting the Hardware
- Chapter 3 External Connection
- Chapter 5 About Software
- Chapter 6 About Hardware

For using the board under an OS such as MS-DOS other than Windows, see the following parts of this manual.

- This chapter Step 2 Setting the Hardware
- Chapter 3 External Connection
- Chapter 6 About Hardware
Step 1 Installing the Software

This section describes how to install the Driver libraries.

Before installing the hardware on your PC, install the Driver libraries from the bundled API-PAC(W32) CD-ROM.

The following description assumes the operating system as Windows XP. Although some user interfaces are different depending on the OS used, the basic procedure is the same.

About the driver to be used

Two digital I/O drivers are available: API-DIO(WDM) and API-DIO(98/PC).

API-DIO(WDM) is a new driver to perform digital I/O under Windows.

API-DIO(WDM) was developed to improve the conventional product version of API-DIO(98/PC) in the ease of use and functionality.

It is advisable to use API-DIO(WDM) for you to use an digital I/O device. API-DIO(WDM) will support new OS and devices in the future but will not support Windows NT 4.0, Windows 95, ISA bus.

Use API-DIO(98/PC) if your operating environment contains such an unsupported piece of software or hardware.

Check the following selection guide to easily select the driver to be used.
Starting the Install Program

(1) **Load the CD-ROM [API-PAC(W32)] on your PC.**

(2) **The API-PAC(W32) Installer window appears automatically.**
   If the panel does not appear, run (CD-ROM drive letter):\AUTORUN.exe.

(3) **Click on the [Install Development or Execution Environment] button.**

* When using the Windows Vista, driver is automatically installed.

⚠️ **CAUTION**

Before installing the software in Windows Vista, XP, Server 2003 and 2000, log in as a user with administrator privileges.
Select API-DIO(WDM)

Selecting API-DIO(WDM)

1. The following dialog box appears to select “Driver to install” and “Install option”, “Usage of driver library”.

2. Select the "Advanced Digital I/O driver".

3. Click on the [Install] button.

* Clicking the [API-DIO] button displays detailed information about API-DIO(WDM) and API-DIO(98/PC).

Run the installation

1. Complete the installation by following the instructions on the screen.

2. The Readme file appears when the installation is complete.
Select API-DIO(98/PC)

Selecting API-DIO(98/PC)

1. The following dialog box appears to select “Driver to install” and “Install option”, “Usage of driver library”.
2. Select “Classic Digital I/O driver”.
3. Click on the [Install] button.

* Clicking on the [API-DIO] button displays detailed information on API-DIO(WDM), API-DIO(98/PC).
2. Setup

PIO-32/32L(PCI)H, PI-64L(PCI)H, PO-64L(PCI)H

Executing the Installation

(1) **Follow the on-screen instructions to proceed to install.**

(2) When the required files have been copied, the “Perform a hardware setup now(API-TOOL Configuration)” and “Show readme file” check boxes are displayed.

**When you are installing the software or hardware for the first time:**
1) Uncheck “Perform a hardware setup now”.
2) Click on the [Finish] button.
   Go to Step 2 to set and plug the hardware.

* When the hardware has already been installed:
   Check “Perform a hardware setup now(API-TOOL Configuration)”, then go to Step 4 “Initializing the Software”.

You have now finished installing the software.
Step 2 Setting the Hardware

This section describes how to set the board and plug it on your PC.

The board has some switches and jumper to be preset.

Check the on-board switches and jumpers before plugging the board into an expansion slot.

The board can be set up even with the factory defaults untouched. You can change board settings later.

Parts of the Board and Factory Defaults

Figure 2.1. shows the names of major parts on the board.

Note that the switch setting shown below is the factory default.

![Diagram of board components with labels for interface connector, board ID settings, and other parts.

Figure 2.1. Component Locations

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Setting the Board ID

If you install two or more boards on one personal computer, assign a different ID value to each of the boards to distinguish them.

The board IDs can be set from 0 - Fh to identify up to sixteen boards.

If only one board is used, the original factory setting (Board ID = 0) should be used.

Setting Procedure

To set the board ID, use the rotary switch on the board. Turn the SW1 knob to set the board ID as shown below.

![Board ID Settings (SW1)](image)

**Figure 2.2. Board ID Settings (SW1)**
2. Setup

Plugging the Board

(1) Before plugging the board, shut down the system, unplug the power code of your PC.
(2) Remove the cover from the PC so that the board can be mounted.
(3) Plug the board into an expansion slot.
(4) Attach the board bracket to the PC with a screw.
(5) Put the cover back into place.

Applicable PCI bus slots

PCI bus slots used in PCs have keys to prevent 5V and 3.3V PCI bus boards from being accidentally plugged into wrong bus slots. This board can be plugged into both of the 5V and 3.3V PCI bus slots.

⚠️ CAUTION

- Do not touch the board's metal plated terminals (edge connector) with your hands. Otherwise, the board may malfunction, overheat, or cause a failure.
  If the terminals are touched by someone's hands, clean the terminals with industrial alcohol.
- Do not install or remove the board to or from the slot while the computer's or expansion unit’s power is turned on.
  Otherwise, the board may malfunction, overheat, or cause a failure.
  Be sure that the personal computer power is turned off.
- Make sure that your PC or expansion unit can supply ample power to all the boards installed.
  Insufficiently energized boards could malfunction, overheat, or cause a failure.
- Power supply from the PCI bus slot at +5V is required.
2. Setup

PIO-32/32L(PCI)H, PI-64L(PCI)H, PO-64L(PCI)H

Step 3 Installing the Hardware

For using an expansion board under Windows, you have to let the OS detect the I/O addresses and IRQ to be used by the board. The process is referred to as installing the hardware.

In the case of using two or more boards, make sure you install one by one with the Found New Hardware Wizard.

Turning on the PC

Turn on the power to your PC.

⚠️ CAUTION

- The board cannot be properly installed unless the resources (I/O addresses and interrupt level) for the board can be allocated. Before attempting to install the board, first determine what PC resources are free to use.

- The resources used by each board do not depend on the location of the PCI bus slot or the board itself. If you remove two or more boards that have already been installed and then remount one of them on the computer, it is unknown that which one of the sets of resources previously assigned to the two boards is assigned to the remounted board. In this case, you must check the resource settings.

When Using API-DIO(WDM)

(1) The “Found New Hardware Wizard” will be started.

Select “No, not this time” and then click the “Next” button.
2. Setup

(2) When “Multimedia Controller” is displayed, select “Install from a list or specific location[Advanced]” and then specify that folder on the CD-ROM which contains the setup information (INF) file to register the board.

When the model name of hardware is displayed, select “Install the software automatically [Recommended]” and then click on the “Next” button.

Source folder

The setup information (INF) file is contained in the following folder on the bundled CD-ROM.

Windows 7 [64, 32bit], Vista [64, 32bit], XP [64, 32bit],
Server 2003 [64, 32bit], 2000 \INF\Wdm\Dio
You have now finished installing the hardware.
2. Setup

When Using API-DIO(98/PC)

(1) The “Found New Hardware Wizard” will be started.

Select “No, not this time” and then click the “Next” button.

(2) Select “Install from a list or specific location[Advanced]” and then click the “Next” button.
(3) Specify that folder on the CD-ROM which contains the setup information (INF) file to register the board.

Source folder

The setup information (INF) file is contained in the following folder on the bundled CD-ROM.

Windows 7 [32bit], Vista [32bit], XP [32bit],
Server 2003 [32bit], 2000 \INF\Win2000\Dio\PCI

Example of specifying the folder for use under Windows XP
2. Setup

CAUTION

In Windows XP, the Hardware Wizard displays the following alert dialog box when you have located the INF file. This dialog box appears, only indicating that the relevant driver has not passed Windows Logo testing, and it can be ignored without developing any problem with the operation of the board.

In this case, click on the [Continue Anyway] button.

* The name of the board you have just added is displayed.
- PIO-32/32L(PCI)H,
- PI-64L(PCI)H,
- PO-64L(PCI)H

You have now finished installing the hardware.
Step 4 Initializing the Software

The driver function library requires the initial setting to recognize the execution environment. It is called the initialization of the Driver library.

When Using API-DIO(WDM)

API-DIO(WDM) is initialized automatically during hardware installation. Therefore, if you want to use it with its initial settings, you can skip the setting procedure described in Step 4. To change the device name, follow the setting procedure shown below.

Setting the device name

1. Run Device Manager. From [My Computer] - [Control Panel], select [System] and then select the [Device Manager] tab.
   (You can also open Device Manager by right clicking on My Computer and selecting Properties.)

   ![Device Manager](image)

   * The name of the board you have just added is displayed.
   - PIO-32/32L(PCI)H,
   - PI-64L(PCI)H,
   - PO-64L(PCI)H

2. The installed hardware appears under the CONTEC Devices node. Open the CONTEC Devices node and select the device you want to setup (the device name should appear highlighted). Click [Properties].
(3) The property page for the device opens.
Enter the device name in the common settings tab page and then click [OK].
The device name you set here is used later when programming.

* The initial device name that appears is a default value. You can use this default name if you wish.
* Make sure that you do not use the same name for more than one device.

You have now finished installing the initial setting of Software.
2. Setup

When Using API-DIO(98/PC)

(1) Open the Start Menu, then select “Programs” – “CONTEC API-PAC(W32)” – “API-TOOL Configuration”.

(2) API-TOOL Configuration detects boards automatically.
   The detected boards are listed.

Updating the Settings

(1) Select “Save setting to registry…” from the “File” menu.

You have now finished installing the initial setting of Software.
Step 5 Checking Operations with the Diagnosis Program

Use the diagnosis program to check that the board and driver software work normally, thereby you can confirm that they have been set up correctly.

What is the Diagnosis Program?

The diagnosis program diagnoses the states of the board and driver software.

It can also be used as a simple checker when an external device is actually connected.

Using the “Diagnosis Report” feature reports the driver settings, the presence or absence of the board, I/O status, and interrupt status.

Check Method

Connect the board to a remote device to test the input/output and check the execution environment. For this board, prepare an external power supply (12 - 24 V).

The Check Mate (CM-64L) comes in handy when you check digital I/O boards.

Check the board with the factory defaults untouched.

Connection diagram

To connect a device other than the Check Mate, see Chapter 3 “External Connection”.
Using the Diagnosis Program

Starting the Diagnosis Program for Use of API-DIO(WDM)

Click the [Diagnosis] button on the device property page to start the diagnosis program.
2. Setup

Starting the Diagnosis Program for Use of API-DIO(98/PC)

Select the board in the API-TOOL Configuration windows, then run the Diagnosis Program. Follow the instructions on screen.

* The name of the board you have just added is displayed.

![API-TOOL Configuration](image)

* The name of the board you have just added is displayed.
- PIO-32/32L(PCI)H,
- PI-64L(PCI)H,
- PO-64L(PCI)H
Checking Digital Inputs and Outputs
The main panel of the Diagnosis Program appears.
You can check the current operation states of the board in the following boxes:

“Input Port” : Displays input values bit by bit at fixed time intervals.
“Output Port” : Mouse operation allows the data to output or display.
“Interrupt” : Displays the number of interrupts detected bit by bit.

To use the wait time control feature, click on the [Wait Configuration] button. Use the feature when the wait time based on the DioWait or DioWaitEx function is not normal.

To use the function execution time measurement feature, click on the [Measurement Time] button. Enter the I/O start port and the number of ports, then press the measurement button. The time for each execution of a function will be measured.
Diagnosis Report

(1) Clicking on the [Show Diagnosis Report] button displays detailed data such as board settings and the diagnosis results while saving them in text format.

When it is API-DIO(WDM), file name to save the result is displayed. When it is API-DIO(98/PC), the result is saved to the folder in which to install (Program Files\CONTEC\API-PAC(W32)) by the text file (DioRep.txt) and then displayed.

The Diagnosis Program performs “board presence/absence check”, “driver file test”, “board setting test”, and so on.

⚠️ CAUTION ⚠️

Before executing diagnosis report output, unplug the cable from the board.

(2) A diagnosis report is displayed as shown below.
Setup Troubleshooting

Symptoms and Actions

No output can be obtained.

Use API-TOOL Configuration to check whether the board name setting is wrong.

The board works with the Diagnosis Program but not with an application.

The Diagnosis Program is coded with API-TOOL functions. As long as the board operates with the Diagnosis Program, it is to operate with other applications as well. In such cases, review your program while paying attention to the following points:

- Check the arguments to functions and their return values.
- When the board is an isolated type, it has a time lag for its response between the output by a function and the actual output. Consider the execution intervals between functions.

The OS won’t normally get started or detect the board.

[Windows 7, Vista, XP, Server 2003, 2000]

Turn off the power to your PC, then unplug the board. Restart the OS and delete the board settings of API-TOOL Configuration. Turn off the PC again, plug the board, and restart the OS. Let the OS detect the board and use API-TOOL Configuration to register board settings.

If your problem cannot be resolved

Contact your retailer.
2. Setup

PIO-32/32L(PCI)H, PI-64L(PCI)H, PO-64L(PCI)H
3. External Connection

This chapter describes the interface connectors on the board and the external I/O circuits. Check the information available here when connecting an external device.

How to connect the connectors

Connector shape

The on-board interface connector (CN1) is used when connecting this product and the external devices.

* Please refer to chapter 1 for more information on the supported cable and accessories.

Figure 3.1. Interface Connector (CN1) Shape
### Connector Pin Assignment

#### Pin Assignments of Interface Connector (CN1)  <PIO-32/32L(PCI)H>

<table>
<thead>
<tr>
<th>Pin Assignments of Interface Connector (CN1)</th>
<th>PIO-32/32L(PCI)H</th>
</tr>
</thead>
<tbody>
<tr>
<td>+7 port (output)</td>
<td>N.C.</td>
</tr>
<tr>
<td>OP 6/7 OP 6/7</td>
<td>N.C.</td>
</tr>
<tr>
<td>+5 port (output)</td>
<td>B57 - B19 A19, A18, A17, A16, A15, A14, A13, A12, A11, A10, A9, A8, A7, A6, A5, A4, A3, A2, A1, A0, IP 0/1</td>
</tr>
<tr>
<td>+4 port (output)</td>
<td>B47 - B09 A09, A08, A07, A06, A05, A04, A03, A02, A01, A00, IP 0/1</td>
</tr>
<tr>
<td>Common minus pin for +4/+5 output ports</td>
<td>N.C.</td>
</tr>
<tr>
<td>ON 4/5 ON 4/5</td>
<td>B54 - B04 A04, A03, A02, A01, A00</td>
</tr>
<tr>
<td>+4 port (output)</td>
<td>N.C.</td>
</tr>
<tr>
<td>+5 port (output)</td>
<td>N.C.</td>
</tr>
</tbody>
</table>

* I-00 - I-37 can be used as interrupt signal. The numbers in square brackets [ ] are pin numbers designated by HONDA TSUSHIN KOGYO CO., LTD.

### Table

<table>
<thead>
<tr>
<th>Pin Assignment</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>I-00 - I-37</td>
<td>32 input signal pins. Connect output signals from the external device to these pins.</td>
</tr>
<tr>
<td>0-40 - 0-77</td>
<td>32 output signal pins. Connect these pins to the input signal pins of the external device.</td>
</tr>
<tr>
<td>IP 0/1 - IP 2/3</td>
<td>Connect the positive side of the external power supply. These pins are common to 16 input signal pins.</td>
</tr>
<tr>
<td>OP 4/5 - OP 6/7</td>
<td>Connect the positive side of the external power supply. These pins are common to 16 output signal pins.</td>
</tr>
<tr>
<td>ON 4/5 - ON 6/7</td>
<td>Connect the negative side of the external power supply. These pins are common to 16 output signal pins.</td>
</tr>
<tr>
<td>N.C.</td>
<td>This pin is left unconnected.</td>
</tr>
</tbody>
</table>

Figure 3.2. Pin Assignments of Interface Connector (CN1)  <PIO-32/32L(PCI)H>
3. External Connection

Pin Assignments of Interface Connector (CN1) <PI-64L(PCI)H>

* I-00 · I-37 can be used as interrupt signal.

The numbers in square brackets [ ] are pin numbers designated by HONDA TSUSHIN KOGYO CO., LTD.

<table>
<thead>
<tr>
<th>Pin Assignment</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>I-00 · I-77 64</td>
<td>64 input signal pins. Connect output signals from the external device to these pins.</td>
</tr>
<tr>
<td>IP 0/1 · IP 6/7</td>
<td>Connect the positive side of the external power supply. These pins are common to 16 input signal pins.</td>
</tr>
<tr>
<td>N.C.</td>
<td>This pin is left unconnected.</td>
</tr>
</tbody>
</table>

Figure 3.3. Pin Assignments of Interface Connector (CN1) <PI-64L(PCI)H>
Pin Assignments of Interface Connector (CN1)  <PO-64L(PCI)H>

The numbers in square brackets [ ] are pin numbers designated by HONDA TSUSHIN KOGYO CO., LTD.

<table>
<thead>
<tr>
<th>Common plus pin for +6/+7 output ports</th>
<th>OP 6/7</th>
<th>B48 A48</th>
<th>OP 2/3</th>
<th>Common plus pin for +2/+3 output ports</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>OP 6/7</td>
<td>B47 A47</td>
<td>OP 2/3</td>
<td></td>
</tr>
<tr>
<td>+7 port (output)</td>
<td>O-77</td>
<td>B46 A46</td>
<td>O-37</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-76</td>
<td>B45 A45</td>
<td>O-36</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-75</td>
<td>B44 A44</td>
<td>O-35</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-74</td>
<td>B43 A43</td>
<td>O-34</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-73</td>
<td>B42 A42</td>
<td>O-33</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-72</td>
<td>B41 A41</td>
<td>O-32</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-71</td>
<td>B40 A40</td>
<td>O-31</td>
<td></td>
</tr>
<tr>
<td>+6 port (output)</td>
<td>O-70</td>
<td>B39 A39</td>
<td>O-30</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-69</td>
<td>B38 A38</td>
<td>O-27</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-68</td>
<td>B37 A37</td>
<td>O-26</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-67</td>
<td>B36 A36</td>
<td>O-25</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-66</td>
<td>B35 A35</td>
<td>O-24</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-65</td>
<td>B34 A34</td>
<td>O-23</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-64</td>
<td>B33 A33</td>
<td>O-22</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-63</td>
<td>B32 A32</td>
<td>O-21</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-62</td>
<td>B31 A31</td>
<td>O-20</td>
<td></td>
</tr>
<tr>
<td>Common minus pin for +6/+7 output ports</td>
<td>ON 6/7</td>
<td>B30 A30</td>
<td>ON 2/3</td>
<td>Common minus pin for +2/+3 output ports</td>
</tr>
<tr>
<td></td>
<td>ON 6/7</td>
<td>B29 A29</td>
<td>ON 2/3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>N.C.</td>
<td>B28 A28</td>
<td>N.C.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>N.C.</td>
<td>B27 A27</td>
<td>N.C.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>N.C.</td>
<td>B26 A26</td>
<td>N.C.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>N.C.</td>
<td>B25 A25</td>
<td>N.C.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>N.C.</td>
<td>B24 A24</td>
<td>N.C.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>N.C.</td>
<td>B23 A23</td>
<td>N.C.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>N.C.</td>
<td>B22 A22</td>
<td>N.C.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>N.C.</td>
<td>B21 A21</td>
<td>N.C.</td>
<td></td>
</tr>
<tr>
<td>+5 port (output)</td>
<td>OP 5/5</td>
<td>B20 A20</td>
<td>OP 0/1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>OP 5/5</td>
<td>B19 A19</td>
<td>OP 0/1</td>
<td></td>
</tr>
<tr>
<td>Common plus pin for +4/+5 output ports</td>
<td>OP 4/5</td>
<td>B18 A18</td>
<td>OP 0/1</td>
<td>Common plus pin for +0/+1 output ports</td>
</tr>
<tr>
<td></td>
<td>OP 4/5</td>
<td>B17 A17</td>
<td>OP 0/1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-56</td>
<td>B16 A16</td>
<td>O-16</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-55</td>
<td>B15 A15</td>
<td>O-15</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-54</td>
<td>B14 A14</td>
<td>O-14</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-53</td>
<td>B13 A13</td>
<td>O-13</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-52</td>
<td>B12 A12</td>
<td>O-12</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-51</td>
<td>B11 A11</td>
<td>O-11</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-50</td>
<td>B10 A10</td>
<td>O-10</td>
<td></td>
</tr>
<tr>
<td>+4 port (output)</td>
<td>O-49</td>
<td>B09 A09</td>
<td>O-09</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-48</td>
<td>B08 A08</td>
<td>O-08</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-47</td>
<td>B07 A07</td>
<td>O-07</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-46</td>
<td>B06 A06</td>
<td>O-06</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-45</td>
<td>B05 A05</td>
<td>O-05</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-44</td>
<td>B04 A04</td>
<td>O-04</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-43</td>
<td>B03 A03</td>
<td>O-03</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-42</td>
<td>B02 A02</td>
<td>O-02</td>
<td></td>
</tr>
<tr>
<td></td>
<td>O-41</td>
<td>B01 A01</td>
<td>O-01</td>
<td></td>
</tr>
<tr>
<td>Common minus pin for +4/+5 output ports</td>
<td>ON 4/5</td>
<td>B00 A00</td>
<td>ON 0/1</td>
<td>Common minus pin for +0/+1 output ports</td>
</tr>
<tr>
<td></td>
<td>ON 4/5</td>
<td>B01 A01</td>
<td>ON 0/1</td>
<td></td>
</tr>
</tbody>
</table>

The numbers in square brackets [ ] are pin numbers designated by HONDA TSUSHIN KOGYO CO., LTD.

<table>
<thead>
<tr>
<th>O-00 · O-77</th>
<th>64 output signal pins. Connect input signals from the external device to these pins.</th>
</tr>
</thead>
<tbody>
<tr>
<td>OP 0/1 · OP 6/7</td>
<td>Connect the positive side of the external power supply. These pins are common to 16 output signal pins.</td>
</tr>
<tr>
<td>ON 0/1 · ON 6/7</td>
<td>Connect the negative side of the external power supply. These pins are common to 16 output signal pins.</td>
</tr>
<tr>
<td>N.C.</td>
<td>This pin is left unconnected.</td>
</tr>
</tbody>
</table>

Figure 3.4. Pin Assignments of Interface Connector (CN1)  <PO-64L(PCI)H>
### 3. External Connection

Pin Assignments of Optional Connector PCB96WS <PIO-32/32L(PCI)H>

- **Optional cable PCB96WS**

<table>
<thead>
<tr>
<th>N.C.</th>
<th>20</th>
<th>1</th>
<th>N.C.</th>
</tr>
</thead>
<tbody>
<tr>
<td>+2 port (Input)</td>
<td>+6 port (Output)</td>
<td>+4 port (Output)</td>
<td></td>
</tr>
<tr>
<td>I-20 21</td>
<td>I-00 1</td>
<td>O-60 21</td>
<td></td>
</tr>
<tr>
<td>I-21 22</td>
<td>I-01 2</td>
<td>O-61 22</td>
<td></td>
</tr>
<tr>
<td>I-22 23</td>
<td>I-02 3</td>
<td>O-62 23</td>
<td></td>
</tr>
<tr>
<td>+3 port (Input)</td>
<td>+6 port (Output)</td>
<td>+5 port (Output)</td>
<td></td>
</tr>
<tr>
<td>I-23 24</td>
<td>I-03 4</td>
<td>O-63 24</td>
<td></td>
</tr>
<tr>
<td>I-24 25</td>
<td>I-04 5</td>
<td>O-64 25</td>
<td></td>
</tr>
<tr>
<td>I-25 26</td>
<td>I-05 6</td>
<td>O-65 26</td>
<td></td>
</tr>
<tr>
<td>I-26 27</td>
<td>I-06 7</td>
<td>O-66 27</td>
<td></td>
</tr>
<tr>
<td>I-27 28</td>
<td>I-07 8</td>
<td>O-67 28</td>
<td></td>
</tr>
<tr>
<td>+0 port (Input)</td>
<td>+1 port (Input)</td>
<td>+7 port (Output)</td>
<td></td>
</tr>
<tr>
<td>I-30 29</td>
<td>I-10 9</td>
<td>O-70 29</td>
<td></td>
</tr>
<tr>
<td>I-31 30</td>
<td>I-11 10</td>
<td>O-71 30</td>
<td></td>
</tr>
<tr>
<td>I-32 31</td>
<td>I-12 11</td>
<td>O-72 31</td>
<td></td>
</tr>
<tr>
<td>I-33 32</td>
<td>I-13 12</td>
<td>O-73 32</td>
<td></td>
</tr>
<tr>
<td>I-34 33</td>
<td>I-14 13</td>
<td>O-74 33</td>
<td></td>
</tr>
<tr>
<td>+3 port (Input)</td>
<td>+1 port (Input)</td>
<td>+5 port (Output)</td>
<td></td>
</tr>
<tr>
<td>I-35 34</td>
<td>I-15 14</td>
<td>O-75 34</td>
<td></td>
</tr>
<tr>
<td>I-36 35</td>
<td>I-16 15</td>
<td>O-76 35</td>
<td></td>
</tr>
<tr>
<td>I-37 36</td>
<td>I-17 16</td>
<td>O-77 36</td>
<td></td>
</tr>
</tbody>
</table>

**Figure 3.5. PCB96WS Signal Assignments <PIO-32/32L(PCI)H>**

Pin Assignments of Optional Connector CCB-96 <PIO-32/32L(PCI)H>

- "Optional cable PCB96FS" + "Connector conversion board CCB-96"

<table>
<thead>
<tr>
<th>N.C.</th>
<th>1</th>
<th>20</th>
<th>N.C.</th>
</tr>
</thead>
<tbody>
<tr>
<td>+0 port (Input)</td>
<td>+2 port (Input)</td>
<td>+4 port (Output)</td>
<td></td>
</tr>
<tr>
<td>I-00 2</td>
<td>21 I-20</td>
<td>O-40 2</td>
<td></td>
</tr>
<tr>
<td>I-01 3</td>
<td>22 I-21</td>
<td>O-41 3</td>
<td></td>
</tr>
<tr>
<td>+0 port (Input)</td>
<td>+4 port (Output)</td>
<td>+6 port (Output)</td>
<td></td>
</tr>
<tr>
<td>I-02 4</td>
<td>23 I-22</td>
<td>O-42 4</td>
<td></td>
</tr>
<tr>
<td>I-03 5</td>
<td>24 I-23</td>
<td>O-43 5</td>
<td></td>
</tr>
<tr>
<td>I-04 6</td>
<td>25 I-24</td>
<td>O-44 6</td>
<td></td>
</tr>
<tr>
<td>+3 port (Input)</td>
<td>+4 port (Output)</td>
<td>+7 port (Output)</td>
<td></td>
</tr>
<tr>
<td>I-05 7</td>
<td>26 I-25</td>
<td>O-45 7</td>
<td></td>
</tr>
<tr>
<td>I-06 8</td>
<td>27 I-26</td>
<td>O-46 8</td>
<td></td>
</tr>
<tr>
<td>I-07 9</td>
<td>28 I-27</td>
<td>O-47 9</td>
<td></td>
</tr>
<tr>
<td>+1 port (Input)</td>
<td>+5 port (Output)</td>
<td>+0 port (Output)</td>
<td></td>
</tr>
<tr>
<td>I-10 10</td>
<td>29 I-30</td>
<td>O-50 10</td>
<td></td>
</tr>
<tr>
<td>I-11 11</td>
<td>30 I-31</td>
<td>O-51 11</td>
<td></td>
</tr>
<tr>
<td>N.C.</td>
<td>19</td>
<td>N.C.</td>
<td>19</td>
</tr>
</tbody>
</table>

**Figure 3.6. CCB-96 Signal Assignments <PIO-32/32L(PCI)H>**
### 3. External Connection

**Pin Assignments of Optional Connector PCB96WS <PI-64L(PCI)H>**

- **Optional cable PCB96WS**

![Diagram of optional connector PCB96WS](image)

<table>
<thead>
<tr>
<th>Connector CN3 (CNA)</th>
<th>Connector CN4 (CNB)</th>
</tr>
</thead>
<tbody>
<tr>
<td>N.C. 1</td>
<td>+2 port (Input)</td>
</tr>
<tr>
<td>N.C. 2</td>
<td>1-00* 21</td>
</tr>
<tr>
<td>N.C. 3</td>
<td>1-01* 22</td>
</tr>
<tr>
<td>N.C. 4</td>
<td>1-02* 23</td>
</tr>
<tr>
<td>N.C. 5</td>
<td>1-03* 24</td>
</tr>
<tr>
<td>N.C. 6</td>
<td>1-04* 25</td>
</tr>
<tr>
<td>N.C. 7</td>
<td>1-05* 26</td>
</tr>
<tr>
<td>N.C. 8</td>
<td>1-06* 27</td>
</tr>
<tr>
<td>N.C. 9</td>
<td>1-07* 28</td>
</tr>
<tr>
<td>+0 port (Input)</td>
<td>1-10* 29</td>
</tr>
<tr>
<td>+1 port (Input)</td>
<td>1-11* 30</td>
</tr>
<tr>
<td>+2 port (Input)</td>
<td>1-12* 31</td>
</tr>
<tr>
<td>+3 port (Input)</td>
<td>1-13* 32</td>
</tr>
<tr>
<td>+4 port (Input)</td>
<td>1-14* 33</td>
</tr>
<tr>
<td>+5 port (Input)</td>
<td>1-15* 34</td>
</tr>
<tr>
<td>+6 port (Input)</td>
<td>1-16* 35</td>
</tr>
<tr>
<td>+7 port (Input)</td>
<td>1-17* 36</td>
</tr>
<tr>
<td>Common plus pin for +2/+3 input ports</td>
<td>IP 2/3 37</td>
</tr>
<tr>
<td>Common plus pin for +0/+1 input ports</td>
<td>IP 0/1 37</td>
</tr>
<tr>
<td>Common plus pin for +4/+5 input ports</td>
<td>IP 4/5 37</td>
</tr>
<tr>
<td>Common plus pin for +6/+7 input ports</td>
<td>IP 6/7 37</td>
</tr>
<tr>
<td>N.C. 19</td>
<td>N.C. 19</td>
</tr>
</tbody>
</table>

*+0 to 1-37 can be used as interrupt signal.*

**Figure 3.7. PCB96WS Signal Assignments <PI-64L(PCI)H>**

**Pin Assignments of Optional Connector CCB-96 <PI-64L(PCI)H>**

- **Optional cable PCB96PS* + Connector conversion board CCB-96**

![Diagram of connector CCB-96](image)

<table>
<thead>
<tr>
<th>Connector CCB-96 (CN3)</th>
<th>Connector CCB-96 (CN4)</th>
</tr>
</thead>
<tbody>
<tr>
<td>N.C. 1</td>
<td>+2 port (Input)</td>
</tr>
<tr>
<td>N.C. 2</td>
<td>1-00* 21</td>
</tr>
<tr>
<td>N.C. 3</td>
<td>1-01* 22</td>
</tr>
<tr>
<td>N.C. 4</td>
<td>1-02* 23</td>
</tr>
<tr>
<td>N.C. 5</td>
<td>1-03* 24</td>
</tr>
<tr>
<td>N.C. 6</td>
<td>1-04* 25</td>
</tr>
<tr>
<td>N.C. 7</td>
<td>1-05* 26</td>
</tr>
<tr>
<td>N.C. 8</td>
<td>1-06* 27</td>
</tr>
<tr>
<td>N.C. 9</td>
<td>1-07* 28</td>
</tr>
<tr>
<td>+3 port (Input)</td>
<td>1-10* 29</td>
</tr>
<tr>
<td>+4 port (Input)</td>
<td>1-11* 30</td>
</tr>
<tr>
<td>+5 port (Input)</td>
<td>1-12* 31</td>
</tr>
<tr>
<td>+6 port (Input)</td>
<td>1-13* 32</td>
</tr>
<tr>
<td>+7 port (Input)</td>
<td>1-14* 33</td>
</tr>
<tr>
<td>Common plus pin for +0/+1 input ports</td>
<td>IP 0/1 37</td>
</tr>
<tr>
<td>Common plus pin for +2/+3 input ports</td>
<td>IP 2/3 37</td>
</tr>
<tr>
<td>Common plus pin for +4/+5 input ports</td>
<td>IP 4/5 37</td>
</tr>
<tr>
<td>Common plus pin for +6/+7 input ports</td>
<td>IP 6/7 37</td>
</tr>
<tr>
<td>N.C. 19</td>
<td>N.C. 19</td>
</tr>
</tbody>
</table>

**Figure 3.8. CCB-96 Signal Assignments <PI-64L(PCI)H>**
Pin Assignments of Optional Connector PCB96WS <PO-64L(PCI)H>

- Optional cable PCB96WS

![Diagram of cable connections]

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
<th>Pin Assignment</th>
</tr>
</thead>
<tbody>
<tr>
<td>CNA</td>
<td>Common minus pin for +2/+3 output ports</td>
<td>ON 2/3 20</td>
</tr>
<tr>
<td></td>
<td>*2 port (Output)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>+0 port (Output)</td>
<td>ON 0/1</td>
</tr>
<tr>
<td></td>
<td>*1 port (Output)</td>
<td>OP 2/3 37</td>
</tr>
<tr>
<td></td>
<td>Common minus pin for +2/+3 output ports</td>
<td>OP 2/3 37</td>
</tr>
<tr>
<td></td>
<td>+0 port (Output)</td>
<td>OP 0/1</td>
</tr>
<tr>
<td></td>
<td>+1 port (Output)</td>
<td>OP 0/1</td>
</tr>
<tr>
<td>CNB</td>
<td>Common minus pin for +2/+3 output ports</td>
<td>ON 6/7 20</td>
</tr>
<tr>
<td></td>
<td>*6 port (Output)</td>
<td>ON 0/1</td>
</tr>
<tr>
<td></td>
<td>+7 port (Output)</td>
<td>ON 0/1</td>
</tr>
<tr>
<td>Common plus pin for +2/+3 output ports</td>
<td>OP 6/7 37</td>
<td></td>
</tr>
<tr>
<td></td>
<td>+4 port (Output)</td>
<td>OP 4/5 18</td>
</tr>
</tbody>
</table>

Figure 3.9. PCB96WS Signal Assignments <PO-64L(PCI)H>

Pin Assignments of Optional Connector CCB-96 <PO-64L(PCI)H>

- "Optional cable PCB96PS" or "Connector conversion board CCB-96"

![Diagram of connector connections]

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
<th>Pin Assignment</th>
</tr>
</thead>
<tbody>
<tr>
<td>CNA</td>
<td>Common minus pin for +0/+1 output ports</td>
<td>ON 0/1 1</td>
</tr>
<tr>
<td></td>
<td>*0 port (Output)</td>
<td>ON 2/3 20</td>
</tr>
<tr>
<td></td>
<td>+2 port (Output)</td>
<td>ON 0/1 1</td>
</tr>
<tr>
<td></td>
<td>+1 port (Output)</td>
<td>OP 2/3 37</td>
</tr>
<tr>
<td></td>
<td>Common minus pin for +0/+1 output ports</td>
<td>OP 2/3 37</td>
</tr>
<tr>
<td>CNB</td>
<td>Common minus pin for +2/+3 output ports</td>
<td>ON 4/5 1</td>
</tr>
<tr>
<td></td>
<td>+4 port (Output)</td>
<td>ON 4/5 1</td>
</tr>
<tr>
<td></td>
<td>+6 port (Output)</td>
<td>ON 6/7 20</td>
</tr>
<tr>
<td></td>
<td>Common plus pin for +4/+5 output ports</td>
<td>OP 6/7 37</td>
</tr>
<tr>
<td></td>
<td>+7 port (Output)</td>
<td>OP 6/7 37</td>
</tr>
</tbody>
</table>

Figure 3.10. CCB-96 Signal Assignments <PO-64L(PCI)H>
3. External Connection

Relationships between API-PAC(W32) Logical Ports/Bits and Connector Signal Pins

The following table lists the relationships between the connector signal pins and the logical port/bit numbers used for I/O functions when applications are written with API-PAC(W32).

PIO-32/32L(PCI)H

Table 3.1. Logical Ports, Logical Bits, and Connector Signal Pins <PIO-32/32L(PCI)H>

<table>
<thead>
<tr>
<th></th>
<th>D7</th>
<th>D6</th>
<th>D5</th>
<th>D4</th>
<th>D3</th>
<th>D2</th>
<th>D1</th>
<th>D0</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input logical port 0</td>
<td>I-07</td>
<td>I-06</td>
<td>I-05</td>
<td>I-04</td>
<td>I-03</td>
<td>I-02</td>
<td>I-01</td>
<td>I-00</td>
</tr>
<tr>
<td></td>
<td>[7]</td>
<td>[6]</td>
<td>[5]</td>
<td>[4]</td>
<td>[3]</td>
<td>[2]</td>
<td>[1]</td>
<td>[0]</td>
</tr>
<tr>
<td>Input logical port 1</td>
<td>I-17</td>
<td>I-16</td>
<td>I-15</td>
<td>I-14</td>
<td>I-13</td>
<td>I-12</td>
<td>I-11</td>
<td>I-10</td>
</tr>
<tr>
<td>Input logical port 2</td>
<td>I-27</td>
<td>I-26</td>
<td>I-25</td>
<td>I-24</td>
<td>I-23</td>
<td>I-22</td>
<td>I-21</td>
<td>I-20</td>
</tr>
<tr>
<td></td>
<td>[23]</td>
<td>[22]</td>
<td>[21]</td>
<td>[20]</td>
<td>[19]</td>
<td>[18]</td>
<td>[17]</td>
<td>[16]</td>
</tr>
<tr>
<td></td>
<td>[31]</td>
<td>[30]</td>
<td>[29]</td>
<td>[28]</td>
<td>[27]</td>
<td>[26]</td>
<td>[25]</td>
<td>[24]</td>
</tr>
<tr>
<td>Output logical port0</td>
<td>O-47</td>
<td>O-46</td>
<td>O-45</td>
<td>O-44</td>
<td>O-43</td>
<td>O-42</td>
<td>O-41</td>
<td>O-40</td>
</tr>
<tr>
<td></td>
<td>[7]</td>
<td>[6]</td>
<td>[5]</td>
<td>[4]</td>
<td>[3]</td>
<td>[2]</td>
<td>[1]</td>
<td>[0]</td>
</tr>
<tr>
<td>Output logical port1</td>
<td>O-57</td>
<td>O-56</td>
<td>O-55</td>
<td>O-54</td>
<td>O-53</td>
<td>O-52</td>
<td>O-51</td>
<td>O-50</td>
</tr>
<tr>
<td>Output logical port2</td>
<td>O-67</td>
<td>O-66</td>
<td>O-65</td>
<td>O-64</td>
<td>O-63</td>
<td>O-62</td>
<td>O-61</td>
<td>O-60</td>
</tr>
<tr>
<td></td>
<td>[23]</td>
<td>[22]</td>
<td>[21]</td>
<td>[20]</td>
<td>[19]</td>
<td>[18]</td>
<td>[17]</td>
<td>[16]</td>
</tr>
<tr>
<td>Output logical port3</td>
<td>O-77</td>
<td>O-76</td>
<td>O-75</td>
<td>O-74</td>
<td>O-73</td>
<td>O-72</td>
<td>O-71</td>
<td>O-70</td>
</tr>
<tr>
<td></td>
<td>[31]</td>
<td>[30]</td>
<td>[29]</td>
<td>[28]</td>
<td>[27]</td>
<td>[26]</td>
<td>[25]</td>
<td>[24]</td>
</tr>
</tbody>
</table>

Note: I-xx represents a CAN input signal; O-xx represents a CNB output signal. [xx] represents a logical bit.
PI-64L(PCI)H

Table 3.2. Logical Ports, Logical Bits, and Connector Signal Pins <PI-64L(PCI)H>

<table>
<thead>
<tr>
<th>Input logical port</th>
<th>D7</th>
<th>D6</th>
<th>D5</th>
<th>D4</th>
<th>D3</th>
<th>D2</th>
<th>D1</th>
<th>D0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>I-07</td>
<td>I-06</td>
<td>I-05</td>
<td>I-04</td>
<td>I-03</td>
<td>I-02</td>
<td>I-01</td>
<td>I-00</td>
</tr>
<tr>
<td>1</td>
<td>I-17</td>
<td>I-16</td>
<td>I-15</td>
<td>I-14</td>
<td>I-13</td>
<td>I-12</td>
<td>I-11</td>
<td>I-10</td>
</tr>
<tr>
<td>2</td>
<td>I-27</td>
<td>I-26</td>
<td>I-25</td>
<td>I-24</td>
<td>I-23</td>
<td>I-22</td>
<td>I-21</td>
<td>I-20</td>
</tr>
<tr>
<td>5</td>
<td>I-57</td>
<td>I-56</td>
<td>I-55</td>
<td>I-54</td>
<td>I-53</td>
<td>I-52</td>
<td>I-51</td>
<td>I-50</td>
</tr>
<tr>
<td>6</td>
<td>I-67</td>
<td>I-66</td>
<td>I-65</td>
<td>I-64</td>
<td>I-63</td>
<td>I-62</td>
<td>I-61</td>
<td>I-60</td>
</tr>
<tr>
<td>7</td>
<td>I-77</td>
<td>I-76</td>
<td>I-75</td>
<td>I-74</td>
<td>I-73</td>
<td>I-72</td>
<td>I-71</td>
<td>I-70</td>
</tr>
</tbody>
</table>

Note: I-xx represent input signals. [xx] represents a logical bit.

PO-64L(PCI)H

Table 3.3. Logical Ports, Logical Bits, and Connector Signal Pins <PO-64L(PCI)H>

<table>
<thead>
<tr>
<th>Output logical port</th>
<th>D7</th>
<th>D6</th>
<th>D5</th>
<th>D4</th>
<th>D3</th>
<th>D2</th>
<th>D1</th>
<th>D0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>O-07</td>
<td>O-06</td>
<td>O-05</td>
<td>O-04</td>
<td>O-03</td>
<td>O-02</td>
<td>O-01</td>
<td>O-00</td>
</tr>
<tr>
<td>1</td>
<td>O-17</td>
<td>O-16</td>
<td>O-15</td>
<td>O-14</td>
<td>O-13</td>
<td>O-12</td>
<td>O-11</td>
<td>O-10</td>
</tr>
<tr>
<td>3</td>
<td>O-37</td>
<td>O-36</td>
<td>O-35</td>
<td>O-34</td>
<td>O-33</td>
<td>O-32</td>
<td>O-31</td>
<td>O-30</td>
</tr>
<tr>
<td>5</td>
<td>O-57</td>
<td>O-56</td>
<td>O-55</td>
<td>O-54</td>
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<tr>
<td>6</td>
<td>O-67</td>
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<td>O-64</td>
<td>O-63</td>
<td>O-62</td>
<td>O-61</td>
<td>O-60</td>
</tr>
<tr>
<td>7</td>
<td>O-77</td>
<td>O-76</td>
<td>O-75</td>
<td>O-74</td>
<td>O-73</td>
<td>O-72</td>
<td>O-71</td>
<td>O-70</td>
</tr>
</tbody>
</table>

Note: O-xx represent output signals. [xx] represents a logical bit.

⚠️ CAUTION ⚠️

The logical port and logical bit numbers are virtual port and bit numbers that enable programming independent of board I/O addresses or board types.

For details, refer to API-DIO HELP available after installing API-PAC(W32).
Connecting Input Signals

Connect the input signals to a device which can be current-driven, such as a switch or transistor output device.

The connection requires an external power supply to feed currents.

The board inputs the ON/OFF state of the current-driven device as a digital value.

Input Circuit

* Input pin represent input signals.

Figure 3.11. Input Circuit <PIO-32/32L(PCI)H>, <PI-64L(PCI)H>

The input circuits of interface blocks of the PIO-32/32L(PCI)H and PI-64L(PCI)H are illustrated in Figure 3.11. The signal inputs are isolated by opto-couplers (ready to accept current sinking output signals). The board therefore requires an external power supply to drive the inputs. The power requirement for each input pin is about 5.1 mA at 24 VDC (about 2.6 mA at 12 VDC).

Connecting a Switch

When the switch is ON, the corresponding bit contains 1.

When the switch is OFF, by contrast, the bit contains 0.

Figure 3.12. An Example to use Input I00 <PIO-32/32L(PCI)H>, <PI-64L(PCI)H>
Output Circuit

Connect the output signals to a current-driven controlled device such as a relay or LED.
The connection requires an external power supply to feed currents.
The board controls turning on/off the current-driven controlled device using a digital value.

Output Circuit

Figure 3.13. Output Circuit <PIO-32/32L(PCI)H>, <PO-64L(PCI)H>
The output circuits of interface blocks of the PIO-32/32L(PCI)H and PO-64L(PCI)H are illustrated in Figure 3.7. The signal output section is an opto-coupler isolated, open-collector output (current sink type). Driving the output section requires an external power supply.
The rated output current per channel is 100 mA at maximum.
The output section can also be connected to a TTL level input as it uses a low-saturated transistor for output. The residual voltage (low-level voltage) between the collector and emitter with the output on is 0.5 V or less at an output current within 50 mA or at most 1.0 V at an output current within 100 mA.
A zener diode is connected to the output transistor for protection from surge voltages. A PolySwitch-based overcurrent protector is provided for every eight output transistors. When the overcurrent protector works, the output section of the board is temporarily disabled. If this is the case, turn off the power to the PC and the external power supply and wait for a few minutes, then turn them on back.

⚠️ CAUTION
When the PC is turned on, all output are reset to OFF.
Connection to the LED

When "1" is output to a relevant bit, the corresponding LED comes on.
When "0" is output to the bit, in contrast, the LED goes out.

Figure 3.14. An Example to use Output O-40 <PIO-32/32L(PCI)H>, <PO-64L(PCI)H>

Example of Connection to TTL Level Input

Figure 3.15. Connection Example of Output and TTL level Input Signal <PIO-32/32L(PCI)H>, <PO-64L(PCI)H>
Connecting the Sink Type Output and Sink Output Support Input

The following example shows a connection between a sink type output (output board) and a sink output support input (input board). Refer to this connection example when you connect such boards to each other.

Figure 3.16. Example of Connecting the Sink Type Output and Sink Output Support Input
4. Function

This section describes the features of the board.

Each function described here can be easily set and executed by using the bundled API function library. For details, refer to API-DIO HELP available after installation.

Data I/O Function

Data Input
When input data is “ON”, “1” is input to the relevant bit.
When the input data is “OFF”, in contrast, “0” is input to the relevant bit

Data Output
When “1” is output to the relevant bit, the corresponding transistor is set to “ON”.
When “0” is output to the relevant bit, in contrast, the corresponding transistor is set to “OFF”.

⚠️ CAUTION
When the PC is turned on, all output are reset to 0 (OFF).

Monitoring Output Data
The <PIO-32/32L(PCI)H> and <PO-64L(PCI)H> can read the state of the data currently being output without affecting the output data.
4. Function

Digital Filter Function
Using this feature, the <PIO-32/32L(PCI)H> and <PI-64L(PCI)H> can apply a digital filter to every input pin, thereby preventing wrong recognition of input signals from being affected by noise or chattering.

Digital Filter Function Principle
The digital filter checks the input signal level during the sampling time of the clock signal. When the signal level remains the same for the digital filter set time, the digital filter recognizes that signal as the input signal and changes the signal level of the PC. If the signal level changes at a frequency shorter than the set time, therefore, the level change is ignored.

Input Signal → Digital Filter → Input to PC

Filter Setting Time

Input Signal

Invalid

Valid

Input to PC

Figure 4.1. Digital Filter Function Principle

Set Digital Filter Time
Set the digital filter time to 0 - 24 (14h). Setting the digital filter time to 0 disables digital filtering. It is set to 0 when the power is turned on. Figure 4.2 shows the relationships between digital filter time settings and the actual digital filter times.

Digital Filter Time[sec.] = 2^n / (8 x 10^6)

n: = setting data (0 - 20)

<table>
<thead>
<tr>
<th>Setting Data (n)</th>
<th>Digital Filter Time</th>
<th>Setting Data (n)</th>
<th>Digital Filter Time</th>
<th>Setting Data (n)</th>
<th>Digital Filter Time</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 (00h)</td>
<td>The filter function is not used.</td>
<td>7 (07h)</td>
<td>16μsec</td>
<td>14 (0Eh)</td>
<td>2.048msec</td>
</tr>
<tr>
<td>1 (01h)</td>
<td>0.25μsec</td>
<td>8 (08h)</td>
<td>32μsec</td>
<td>15 (0Fh)</td>
<td>4.096msec</td>
</tr>
<tr>
<td>2 (02h)</td>
<td>0.5μsec</td>
<td>9 (09h)</td>
<td>64μsec</td>
<td>16 (10h)</td>
<td>8.192msec</td>
</tr>
<tr>
<td>3 (03h)</td>
<td>1μsec</td>
<td>10 (0Ah)</td>
<td>128μsec</td>
<td>17 (11h)</td>
<td>16.384msec</td>
</tr>
<tr>
<td>4 (04h)</td>
<td>2μsec</td>
<td>11 (0Bh)</td>
<td>256μsec</td>
<td>18 (12h)</td>
<td>32.768msec</td>
</tr>
<tr>
<td>5 (05h)</td>
<td>4μsec</td>
<td>12 (0Ch)</td>
<td>512μsec</td>
<td>19 (13h)</td>
<td>65.536msec</td>
</tr>
<tr>
<td>6 (06h)</td>
<td>8μsec</td>
<td>13 (0Dh)</td>
<td>1.024msec</td>
<td>20 (14h)</td>
<td>131.072msec</td>
</tr>
</tbody>
</table>

Figure 4.2. Digital Filter Time and Setting Data

⚠️ CAUTION
- If you set the digital filter time, the filter applies to all input pins. You cannot apply the filter only to a specific filter.
- Do not set Setting Data to a value outside the above range as doing so can cause the board to malfunction.

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PIO-32/32L(PCI)H, PI-64L(PCI)H, PO-64L(PCI)H
Interrupt Control Function

The PIO-32/32L(PCI)H can use all of the input signals as interrupt request signals.
The <PI-64L(PCI)> can use up to 32 channels of input signals as interrupt request signals. The input signals available as interrupt request signals are I-00 - I-37.
The board can generate an interrupt request signal to the PC when the input signal change from High to Low or from Low to High.
When the digital filter (described above) is used, interrupt requests are generated by input signals that have passed through the filter.

Disabling/enabling Interrupts

Interrupt mask bits can be used to disable or enable the individual bits for interruptions.
Once a certain bit has been interrupt-disabled, no interrupt occurs even when the corresponding input signal changes its level.
To let interrupts occur, enable the corresponding interrupt mask bit for interruptions.

⚠️ CAUTION

All of the interrupt mask bits are interrupt-disabled when the power is turned on.

Selecting the Edge of input signals, at which to generate an interrupt

The input signal edge selection bit can be used to set the input logic for interruption bit by bit.
If you set an input signal edge selection bit to 0, an interrupt occurs when the input value to the corresponding bit changes from 0 to 1 (at the fall of the input signal from High to Low).
If you set an input signal edge selection bit to 1, an interrupt occurs when the input value to the corresponding bit changes from 1 to 0 (at the rise of the input signal from Low to High).

⚠️ CAUTION

All of the input signal edge selection bits are set to 0 when the power is turned on.
Clearing the Interrupt Status and Interrupt Signal

Interrupt status bits are used to identify the input signal bit being used for requesting an interrupt. When an interrupt status is input, the interrupt request signal and the interrupt status are cleared automatically.

⚠️ CAUTION

- All of the interrupt status bits are set to 0 when the power is turned on.
- If an interrupt mask bit has been set to disable interrupts, the interrupt status bit is not set even when the input signal changes its level.
5. About Software

CD-ROM Directory Structure

\ |
|– Autorun.exe Installer Main Window
| Readmej.html Version information on each API-TOOL (Japanese)
| Readmeu.html Version information on each API-TOOL (English)

|–––APIPAC Each installer
| |–––AIO
| | |–––DISK1
| | |–––DISK2
| | |–––……
| | |–––DISKN
| |–––AioWdm
| |–––CNT
| |–––DIO
| |–––……

|––HELP HELP file
| |–––Aio
| |–––Cnt
| |–––……

|––INF Each INF file for OS
| |–––WDM
| |–––Win2000
| |–––Win95

|––linux Linux driver file
| |–––cnt
| |–––dio
| |–––……

|––Readme Readme file for each driver

|––Release Driver file on each API-TOOL
| |–––API_NT (For creation of a user-specific install program)
| |–––API_W95

|––UsersGuide Hardware User's Guide(PDF files)
About Software for Windows

The bundled CD-ROM “Driver Library API-PAC(W32)” contains the functions that provide the following features:
- Digital input/output of specified ports
- Hardware digital input/output of specified bits
- Hardware digital filtering that prevents wrong recognition of input signals from being affected by noise or chattering.

For details, refer to the help file. The help file provides various items of information such as “Function Reference”, “Sample Programs”, and “FAQs”. Use them for program development and troubleshooting.

Accessing the Help File

(1) Click on the [Start] button on the Windows taskbar.
(2) Using the API-DIO(WDM), from the Start Menu, select “Programs” – “CONTEC API-PAC(W32)” – “DIOWDM” - “API-DIO(WDM) HELP” to display help information.
(3) Using the API-DIO(98/PC), from the Start Menu, select “Programs” – “CONTEC API-PAC(W32)” - “Dio” - “API-DIO HELP” to display help information.

Using Sample Programs

Sample programs have been prepared for specific basic applications.

For the API-DIO(WDM), The sample programs are stored in
\Program Files\CONTEC\API-PAC(W32)\DIOWDM\Sample.

For the API-DIO(98/PC), The sample programs are stored in
\Program Files\CONTEC\API-PAC(W32)\Dio\Samples.

To use each sample program, enter its driver number and group number set by API-TOOL Configuration in the DrvNo and GrpNo fields.

Use these sample programs as references for program development and operation check.

Running a Sample Program

(1) Click on the [Start] button on the Windows taskbar.
(2) For the API-DIO(WDM), from the Start Menu, select “Programs” – “CONTEC API-PAC(W32)” – “DIOWDM” – “SAMPLE…”.
(3) For the API-DIO(98/PC), from the Start Menu, select “Programs” – “CONTEC API-PAC(W32)” – “Dio” – “SAMPLE…”.
(4) A sample program is invoked.
Sample Programs – Examples

API-DIO(WDM) sample program
- Simple I/O sample program : Input digital data through a specified port.
- Multi ports/bits I/O sample program : Input digital data through a specified multi ports/bits.
- Trigger monitoring sample program : Monitoring rising/falling trigger through a specified board.
- Interrupt sample program : Services interrupts of a specified board.

API-DIO(98/PC) sample program
- Sample program 1 : Inputs digital data through a specified port.
- Sample program 2 : Outputs digital data through a specified port.
- Sample program 3 : Inputs/outputs digital data from/to a programmable board.
- Sample program 4 : Inputs digital data from a specified port in the background.
- Sample program 5 : Inputs/outputs digital data from/to a specified bit.
- Sample program 6 : Services interrupts of a specified board.
- Sample program 7 : Provides process control of a specified board.
- Sample program 8 : Performs trigger monitoring of a specified board.
- Sample program 9 : Inputs/outputs digital data through a specified port using BCD data.
- Sample program 10 : Executes digital input/output (simple functions) at specified bits through a specified port.
- Sample program 11 : Services interrupts of a specified board (using an extended function).
- Sample program (Console): Inputs/outputs digital data through a specified port.
Uninstalling the Driver Libraries

To uninstall API-PAC(W32), follow the procedure below.

1. Click on the [Start] button on the Windows taskbar. From the Start Menu, select “Control Panel”.
2. Double-click on “Add or Remove Programs” in the Control Panel.
3. For use of API-DIO(WDM), select “CONTEC API-DIO(WDM) driver” and “CONTEC API-DIO(WDM) VerX.XX (Develop)” from the application list displayed. For use of API-DIO(98/PC), select “CONTEC API-DIO(98/PC)xx VerX.XX (Develop)” and “CONTEC API-DIO(98/PC)xx VerX.XX (Runtime)” from the application list displayed. Click on the [Change/Remove] button. Follow the on-screen instructions to uninstall the function libraries.
About Software for Linux

The Linux version of digital I/O function driver, API-DIO(LNX), provides functions that execute the following features:

- Digital input/output of specified ports
- Digital input/output of specified bits
- Hardware digital filtering that prevents wrong recognition of input signals from being affected by noise or chattering.

For details, refer to the help file. The help file provides various items of information such as “Function Reference”, “Sample Programs”, and “FAQs”. Use them for program development and troubleshooting.

Driver Software Install Procedure

The Linux version for digital I/O driver, API-DIO(LNX), is supplied as a compressed file /linux/dio/cdioXXX.tgz on the bundled API-PAC(W32)CD-ROM. (Note: XXX represents the driver version.)

Mount the CD-ROM as shown below, copy the file to an arbitrary directory, and decompress the file to install the driver.

For details on using the driver, refer to readme.txt and the help file in HTML format extracted by installation.

To install the driver, log in as a superuser.

Decompression and setup procedure

```bash
# cd
# mount /dev/cdrom /mnt/cdrom Mount the CD-ROM.
# cp /mnt/cdrom/linux/dio/cdioXXX.tgz ./
# tar xvfz cdioXXX.tgz Decompress the compressed file.

# cd contec/cdio
# make Compile the file.

# make install Install.

# cd config
# ./config Set up the board to be used.

# ./contec_dio_start.sh Start the driver.
```

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PIO-32/32L(PCI)H, PI-64L(PCI)H, PO-64L(PCI)H
5. About Software

Accessing the Help File

(1) Invoke a web browser in your X-Window environment.
(2) In the browser, open diohelp.htm in the the contec/cdio/help directory.

Using Sample Programs

Sample programs have been prepared for specific basic applications.
Sample programs for each language are contained in the contec/cdio/samples directory. For compiling them, refer to the manual for the desired language.

Uninstalling the driver

To uninstall the driver, use the uninstall shell script contained in the contec/cdio directory. For details, check the contents of the script.
6. About Hardware

This chapter provides hardware specifications and hardware-related supplementary information.

For detailed technical information

For further detailed technical information (“Technical Reference” including the information such as an I/O map, configuration register, etc.), visit the Contec’s web site (http://www.contec.com/support/) to call for it.

Hardware specification

PIO-32/32L(PCI)H

Table 6.1. Specification <PIO-32/32L(PCI)H> <1/2>

<table>
<thead>
<tr>
<th>Item</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input</td>
<td>Opto-coupler isolated input (Compatible with current sink output)(Negative logic *1)</td>
</tr>
<tr>
<td>Number of input signal channels</td>
<td>32 channels (all available for interrupts) (One common power supply per 16 channels)</td>
</tr>
<tr>
<td>Input resistance</td>
<td>4.7kΩ</td>
</tr>
<tr>
<td>Input ON current</td>
<td>2.0mA or more</td>
</tr>
<tr>
<td>Input OFF current</td>
<td>0.16mA or less</td>
</tr>
<tr>
<td>Interrupt</td>
<td>32 interrupt input signals are arranged into a single output of interrupt signal INTA. An interrupt is generated at the rising edge (HIGH-to-LOW transition) or falling edge (LOW-to-HIGH transition).</td>
</tr>
<tr>
<td>Response time</td>
<td>200μsec within</td>
</tr>
</tbody>
</table>

Output

<table>
<thead>
<tr>
<th>Output format</th>
<th>Opto-coupler isolated open-collector output (current sink type)(Negative logic *1)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of output signal channels</td>
<td>32 channels (One common power supply per 16 channels)</td>
</tr>
<tr>
<td>Output voltage</td>
<td>35VDC (Max.)</td>
</tr>
<tr>
<td>Output current</td>
<td>100mA (per channel) (Max.)</td>
</tr>
<tr>
<td>Residual voltage with output on</td>
<td>0.5V or less (Output current≤50mA), 1.0V or less (Output current≤100mA)</td>
</tr>
<tr>
<td>Surge protector</td>
<td>Zener diode RD47FM(NEC) or equivalent</td>
</tr>
<tr>
<td>Response time</td>
<td>200μsec within</td>
</tr>
</tbody>
</table>

Common

| I/O address                  | Any 32-byte boundary                                                            |
| Interruption level           | 1 level use                                                                      |
| Max. board count for connection | 16 boards including the master board                                          |
| Dielectric strength          | 500Vrms                                                                         |
| External circuit power supply | 12 - 24VDC(±10%)                                                                |
Table 6.1. Specification < PIO-32/32L(PCI)H> <2/2>

<table>
<thead>
<tr>
<th>Item</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Common</td>
<td></td>
</tr>
<tr>
<td>Power consumption</td>
<td>5VDC 250mA (Max.)</td>
</tr>
<tr>
<td>Operating condition</td>
<td>0 - 50°C, 10 - 90%RH (No condensation)</td>
</tr>
<tr>
<td>Allowable distance of signal extension</td>
<td>Approx. 50m (depending on wiring environment)</td>
</tr>
<tr>
<td>PCI bus specification</td>
<td>32bit, 33MHz, Universal key shapes supported *2</td>
</tr>
<tr>
<td>Dimension (mm)</td>
<td>176.41(L) x 105.68(H)</td>
</tr>
<tr>
<td>Weight</td>
<td>215g</td>
</tr>
</tbody>
</table>

*1 Data “0” and “1” correspond to the High and Low levels, respectively.

*2 This board requires power supply at +5 V from an expansion slot (it does not work on a machine with a +3.3-V power supply alone).

Board Dimensions

![Board Dimensions Diagram]

The standard outside dimension (L) is the distance from the end of the board to the outer surface of the slot cover.
### Table 6.2. Specification < PI-64L(PCI)H>

<table>
<thead>
<tr>
<th>Item</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Input</strong></td>
<td></td>
</tr>
<tr>
<td>Input format</td>
<td>Opto-coupler isolated input (Compatible with current sink output) (Negative logic *1)</td>
</tr>
<tr>
<td>Number of input signal channels</td>
<td>64 channels (32 channels available for interrupts) (One common power supply per 16 channels)</td>
</tr>
<tr>
<td>Input resistance</td>
<td>4.7kΩ</td>
</tr>
<tr>
<td>Input ON current</td>
<td>2.0mA or more</td>
</tr>
<tr>
<td>Input OFF current</td>
<td>0.16mA or less</td>
</tr>
<tr>
<td>interrupt</td>
<td>32 interrupt input signals are arranged into a single output of interrupt signal INTA. An interrupt is generated at the rising edge (HIGH-to-LOW transition) or falling edge (LOW-to-HIGH transition).</td>
</tr>
<tr>
<td>Response time</td>
<td>200μsec within</td>
</tr>
<tr>
<td><strong>Common</strong></td>
<td></td>
</tr>
<tr>
<td>I/O address</td>
<td>Any 32-byte boundary</td>
</tr>
<tr>
<td>Interruption level</td>
<td>1 level use</td>
</tr>
<tr>
<td>Max. board count for connection</td>
<td>16 boards including the master board</td>
</tr>
<tr>
<td>Dielectric strength</td>
<td>500Vrms</td>
</tr>
<tr>
<td>External circuit power supply</td>
<td>12 - 24VDC(±10%)</td>
</tr>
<tr>
<td>Power consumption</td>
<td>5VDC 250mA (Max.)</td>
</tr>
<tr>
<td>Operating condition</td>
<td>0 - 50°C, 10 - 90%RH (No condensation)</td>
</tr>
<tr>
<td>Allowable distance of signal extension</td>
<td>Approx. 50m (depending on wiring environment)</td>
</tr>
<tr>
<td>PCI bus specification</td>
<td>32bit, 33MHz, Universal key shapes supported *2</td>
</tr>
<tr>
<td>Dimension (mm)</td>
<td>176.41(L) x 105.68(H)</td>
</tr>
<tr>
<td>Weight</td>
<td>215g</td>
</tr>
</tbody>
</table>

*1 Data “0” and “1” correspond to the High and Low levels, respectively.

*2 This board requires power supply at +5 V from an expansion slot (it does not work on a machine with a +3.3-V power supply alone).

### Board Dimensions

![Board Dimensions Diagram](image)

The standard outside dimension (L) is the distance from the end of the board to the outer surface of the slot cover.
### 6. About Hardware

**PO-64L(PCI)H**

**Table 6.3. Specification < PO-64L(PCI)H>**

<table>
<thead>
<tr>
<th>Item</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Output</td>
<td></td>
</tr>
<tr>
<td>Output format</td>
<td>Opto-coupler isolated open-collector output (current sink type)(Negative logic *1)</td>
</tr>
<tr>
<td>Number of output</td>
<td>64 channels(One common power supply per 16 channels)</td>
</tr>
<tr>
<td>signal channels</td>
<td></td>
</tr>
<tr>
<td>Output rating</td>
<td></td>
</tr>
<tr>
<td>Output voltage</td>
<td>35VDC (Max.)</td>
</tr>
<tr>
<td>Output current</td>
<td>100mA (par channel) (Max.)</td>
</tr>
<tr>
<td>Residual voltage</td>
<td>0.5V or less (Output current≤50mA), 1.0V or less (Output current≤100mA)</td>
</tr>
<tr>
<td>with output on</td>
<td></td>
</tr>
<tr>
<td>Surge protector</td>
<td>Zener diode RD47FM(NEC) or equivalent</td>
</tr>
<tr>
<td>Response time</td>
<td>200μsec within</td>
</tr>
<tr>
<td>Common</td>
<td></td>
</tr>
<tr>
<td>I/O address</td>
<td>Any 32-byte boundary</td>
</tr>
<tr>
<td>Interruption level</td>
<td>Not used</td>
</tr>
<tr>
<td>Max. board count for</td>
<td>16 boards including the master board</td>
</tr>
<tr>
<td>connection</td>
<td></td>
</tr>
<tr>
<td>Dielectric strength</td>
<td>500Vrms</td>
</tr>
<tr>
<td>External circuit</td>
<td>12 - 24VDC (±10%)</td>
</tr>
<tr>
<td>power supply</td>
<td></td>
</tr>
<tr>
<td>Power consumption</td>
<td>5VDC 250mA (Max.)</td>
</tr>
<tr>
<td>Operating condition</td>
<td>0 - 50°C, 10 - 90%RH (No condensation)</td>
</tr>
<tr>
<td>Allowable distance of</td>
<td>Approx. 50m (depending on wiring environment)</td>
</tr>
<tr>
<td>signal extension</td>
<td></td>
</tr>
<tr>
<td>PCI bus specification</td>
<td>32bit, 33MHz, Universal key shapes supported *2</td>
</tr>
<tr>
<td>Dimension (mm)</td>
<td>176.41(L) x 105.68(H)</td>
</tr>
<tr>
<td>Weight</td>
<td>215g</td>
</tr>
</tbody>
</table>

*1 Data “0” and “1” correspond to the High and Low levels, respectively.

*2 This board requires power supply at +5 V from an expansion slot (it does not work on a machine with a +3.3-V power supply alone).

**Board Dimensions**

![Board Dimensions Diagram](image)

The standard outside dimension (L) is the distance from the end of the board to the outer surface of the slot cover.
6. About Hardware

Block Diagram

PIO-32/32L(PCI)H

![Block Diagram PIO-32/32L(PCI)H]

Figure 6.1. Block Diagram < PIO-32/32L(PCI)H >
PI-64L(PCI)H

Figure 6.2. Block Diagram < PI-64L(PCI)H>
PO-64L(PCI)H

Figure 6.3. Block Diagram < PO-64L(PCI)H>
Differences between the PIO-32/32L(PCI)H and PIO-32/32L(PCI)

The PIO-32/32L(PCI)H is connector-pin compatible with the conventional PIO-32/32L(PCI) but has the following differences from it:

1. Different in the number of input signals available to interrupt requests
   - PIO-32/32L(PCI)H: All of 32 channels
   - PIO-32/32L(PCI): 4 channels

2. Different in the expression to calculate the digital filter time (n: setting value)
   - PIO-32/32L(PCI)H: $2^n / (8 \times 10^6)$
   - PIO-32/32L(PCI): $2^n / (16 \times 10^6)$

3. Protective elements provided for outputs
   - PIO-32/32L(PCI)H: Surge protector: Zener diode
   - PIO-32/32L(PCI): Nothing

4. Different in interrupt level resource allocation
   - PIO-32/32L(PCI)H: Automatically allocates on interrupt level.
   - PIO-32/32L(PCI): Uses a jumper switch to select whether to allocate interrupt levels.

Differences between the PI-64L(PCI)H and PI-64L(PCI)

The PI-64L(PCI)H is connector-pin compatible with the conventional PI-64L(PCI) but has the following differences from it:

1. Different in the number of input signals available to interrupt requests
   - PI-64L(PCI)H: 32 channels
   - PI-64L(PCI): 4 channels

2. Different in the expression to calculate the digital filter time (n: setting value)
   - PI-64L(PCI)H: $2^n / (8 \times 10^6)$
   - PI-64L(PCI): $2^n / (16 \times 10^6)$

3. Different in interrupt level resource allocation
   - PI-64L(PCI)H: Automatically allocates on interrupt level.
   - PI-64L(PCI): Uses a jumper switch to select whether to allocate interrupt levels.
Differences between the PO-64L(PCI)H and PO-64L(PCI)

The PO-64L(PCI)H is connector-pin compatible with the conventional PO-64L(PCI) but has the following differences from it:

(1) Protective elements provided for outputs
   - PO-64L(PCI)H : Surge protector: Zener diode
   - PO-64L(PCI) : Nothing

(2) Different in interrupt level resource allocation
   - PO-64L(PCI)H : Automatically allocates on interrupt level.
   - PO-64L(PCI) : Uses a jumper switch to select whether to allocate interrupt levels.